Two Day
Training Program
On
"Implementation of Space Vector Pulse Width Modulation for Multilevel Inverters using FPGA"
16th - 17th September, 2016

Sponsored by
Technical Education Quality Improvement Programme (TEQIP Phase-II)

REGISTRATION FORM

Name: Prof/Dr/Mr/Ms_____
Designation:_____
OrganizationAddress:_____
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Tel : _____; Fax : _____
Address for communication:
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Mobile:_____
E-mail:_____
Registration Fee: Rs._____
D.D.No._____; Dated:_____
Drawee Bank:_____
Date: _____
Place:_____

Signature

(Photocopies of registration form are acceptable)

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Vice Chancellor
Osmania University

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Principal, University College of Engineering, OU

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Principal Investigator, SERB- Research Project

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Two Day Training Program

On

"Implementation of Space Vector Pulse Width Modulation for Multilevel Inverters using FPGA"

A Technology Transfer under SERB-Research Project

16th to 17th September, 2016

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Technical Education Quality Improvement Programme (TEQIP Phase-II)

Organized by
Department of Electrical Engineering
University College of Engineering (A)
Osmania University
Hyderabad, Telangana State-500 007
Ph: 040-27098628

About the Electrical Engineering Department:

The department started in 1949 to offer B.E in Electrical Engineering. The Post-graduate course in Electrical Machines was started in 1966. With a view to provide diversity and industrial orientation to the Post Graduate program, the department is offering M.E. courses in Industrial Drives & Control and Power Systems from 1971 and a new PG program in Power Electronic Systems from 2008. Department also offers part time PG courses in Industrial Drives & Control and Power Systems for the working academicians and engineers. The Full-Time and Part-Time Ph.D. programs are being offered in the department.

The department has 23 faculty members who are highly experienced and actively involved in various research activities. The department is also equipped with state-of-art equipment and well qualified technical staff. The First National Power System Conference was conducted by this department for the first time in 1981 and also the 16th National Power System Conference was organized in 2010. The department also celebrated its Diamond Jubilee in year 2009-10. The department is accredited by NBA for 5 years for B.E. program and 3 years for M.E. program from 2013. The department is funded by UGC and SERB for two major research projects in the area of Multilevel Inverters.

The Research lab is established in the department for Multilevel Inverters to cater the needs of faculty, research scholars and industry. As a part of technology transfer under SERB-Research project, this training programme is specially designed for the researchers, who are working in the area of pulse width modulation techniques and multilevel inverters.

Introduction:

This two day training Program on implementation of space vector pulse width modulation for multilevel inverters aims at exposing the participants to various recent developments in the power electronics and drives and its applications in Electrical Engineering. Today there are several applications where FPGA's can provide better solutions than ASIC's and conventional micro-processors. With advent of FPGA's we have entered the era of reconfigurable computing. The FPGA's act as a sort of system-level silicon super-glue sitting at the periphery of circuit board making any thing to connect to anything else, so that we could adopt on the fly and change our design to leverage any new, improved component without having to abandon the rest of the design. Through this course work we try to provide interesting insights in to the world of FPGA's. This training program will provide an understanding of design and implementation flow involved in FPGA. The training module provides various pulse width modulation techniques, multilevel inverters and hands on FPGA tool usage experience. Hence, this TEQIP Sponsored faculty development program aims to foster discussion on recent advances on power electronics and its applications.

Course Contents:

- Overview of Power Electronics
- Thrust areas of Research in Power Electronics and drives
- SVPWM and advanced PWM techniques
- Multilevel Inverters
- FPGA programming and hands on experience

Eligibility:

This training programme is open to faculty, research scholars, P.G. students from the institutions recognized/approved by AICTE, engineers from industry.

Fee Particulars:

M.Tech./M.E. and Ph.D. Scholars	Rs.1000/-
Teachers from Academic Institutes	Rs.1500/-
Engineers from Industry	Rs.2000/-

D.D. should be paid in the favour of "The Head, Electrical Engineering Department, University College of Engineering, Osmania University, Hyderabad, Telangana State". Filled-in application forms may also be sent by e-mail with the scanned copy of DD to *satish_8020@yahoo.co.in*. Original DD should be submitted at the time of registration. The participants are requested to make their own arrangements for accommodation and transport.

Important Dates:

Last date for Receipt of applications: 14/09/16
Intimation to Selected Candidates: 15/09/16

Contact Person:

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Principal Investigator, SERB-Research Project
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